PATENT NUMBER and ISSUE DATE

U.S. UTILITY Patent Application

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GERMANY 10100433.8 01/08/2001

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PG-PUB DO NOT PUBLISH ☐ | RESCIND ☐

Foreign priority claimed ☐ yes ☐ no

35 USC 119 conditions met ☐ yes ☐ no

Verified and Acknowledged Examiners's Initials

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TITLE : Method of circuit verification in digital design

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NOTICE OF ALLOWANCE MAILED	Assistant Examiner	CLAIMS ALLOWED: Total Claims / Print Claim for O.G
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☐ TERMINAL DISCLAIMER	PREPARED FOR ISSUE	Applicati n Examiner

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